

Notice of Allowability	Application No.	Applicant(s)
	10/747,674	SON ET AL.
	Examiner Thuy V. Tran	Art Unit 2821

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to amendment submitted on 08/16/2005.

2. The allowed claim(s) is/are 16-68.

3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some* c) None of the:

1. Certified copies of the priority documents have been received.

2. Certified copies of the priority documents have been received in Application No. 10/106,069.

3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.

(a) including changes required by the Notice of Draftperson's Patent Drawing Review (PTO-948) attached
1) hereto or 2) to Paper No./Mail Date _____.

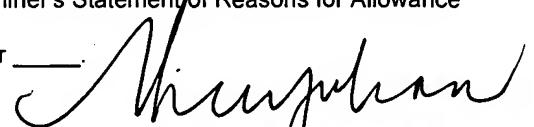
(b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- 1. Notice of References Cited (PTO-892)
- 2. Notice of Draftperson's Patent Drawing Review (PTO-948)
- 3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
- 4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
- 5. Notice of Informal Patent Application (PTO-152)
- 6. Interview Summary (PTO-413),
Paper No./Mail Date _____
- 7. Examiner's Amendment/Comment
- 8. Examiner's Statement of Reasons for Allowance
- 9. Other _____



THUY V. TRAN
PRIMARY EXAMINER

DETAILED ACTION

This is a response to the Applicants' amendment submitted on August 16th, 2005. In virtue of this amendment:

- Claims 1-15 have been canceled;
- Claims 16-38 have been remaining;
- Claims 39-68 are newly added; and thus,
- Claims 16-68 are now presented in the instant application.

Allowable Subject Matter

1. Claims 16-68 are allowed.

Reasons for Allowance

2. The following is an examiner's statement of reasons for allowance:

Prior art fails to disclose or fairly suggest:

- A method for driving a PDP wherein a discharge space between cells is discharged before the initializing step if the predetermined period for the initializing step is present between the discharging step for a preceding field and the initializing step, in combination with the remaining claimed limitations as called for in independent claim 16 (claim 17 is also allowed since it is dependent on claim 16);
- A method for driving a PDP wherein a reset stabilization period for inducing discharging in a discharge space in a cell is additionally performed before the reset period based on information in the preceding field period, in combination with the remaining claimed limitations as called for in independent claim 18 (claims 19-20 are also allowed since they are dependent on claim 18);

- A method for driving a PDP wherein a reset stabilization period for inducing discharging in a discharge space in a cell is additionally performed before the reset period based on a preceding period, in combination with the remaining claimed limitations as called for in independent claim 21 (claims 22-23 are also allowed since they are dependent on claim 21);
- A method of driving a plasma display panel in which a rest period, which is determined by a difference between a frame period and a sum of the reset periods, the address periods and the sustain periods in the frame period, is positioned in the middle of the sustain period or the address period, in combination with the remaining claimed limitations as called for in independent claim 24 (claim 25 is also allowed since it is dependent on claim 24);
- A plasma display panel driving apparatus wherein if cell discharging does not occur for a time interval before application of the reset signal, the reset signal generator generates a reset stabilization signal to cause discharging to occur in the cells prior to the generation of the reset signal, in combination with the remaining claimed limitations as called for in independent claim 26 (claim 39 is also allowed since it is dependent on claim 26);
- A plasma display panel driving apparatus wherein if a signal different from the sustain signal is applied for a time before application of the reset signal, a reset signal generator generates a reset stabilization signal in the cells prior to the reset signal, in combination with the remaining claimed limitations as called for in independent claim 27 (claims 40-41 are also allowed since they are dependent on claim 27);

- A plasma display panel apparatus wherein if a rest period determined by a difference between a frame period and a sum of the reset periods, the address periods and the sustain periods in the frame period is present in a field consisting of the reset period, the address period, and the sustain period, the signal synthesizer synthesizes the reset signal, the address signal, and the sustain signal such that the rest period is positioned in the middle of the sustain period or the address period, in combination with the remaining claimed limitations as called for in independent claim 28;
- A method of driving a PDP wherein the timing of the reset stabilization signal is based on an immediately proceeding period, in combination with the remaining claimed limitations as called for in independent claim 29 (claims 30-33 and 38 are also allowed since they are dependent on claim 29);
- A method of driving a PDP wherein the timing of the reset stabilization signal is based on information in the field period, in combination with the remaining claimed limitations as called for in independent claim 34 (claims 35-37 are also allowed since they are dependent on claim 34);
- A method for driving a plasma display panel in which a reset stabilization period applying a reset stabilization signal having a waveform different from the waveform of the sustain signal for inducing discharging in a discharge space is additionally performed before the reset signal, in combination with the remaining claimed limitations as called for in independent claim 43 (claims 44-51 are also allowed since they are dependent on claim 43);

- A plasma display panel driving apparatus wherein if cell discharging does not occur for a time interval before application of the reset signal, a reset stabilization signal generator generates a reset stabilization signal having the waveform different from the waveform of the sustain signal to cause discharging to occur in the cells prior to the reset signal, in combination with the remaining claimed limitations as called for in independent claim 52 (claims 53-59 are also allowed since they are dependent on claim 52); and
- A plasma display panel driving apparatus comprising a signal synthesizing circuit for synthesizing the signals and for applying the signals to each electrode and for positioning a rest period, in which a signal which is different from the sustain signal is applied, between the periods, wherein a reset stabilization signal generator generates a reset stabilization signal to cause discharging in the cells before application of the reset signal, in combination with the remaining claimed limitations as called for in independent claim 60 (claims 61-68 are also allowed since they are dependent on claim 60).

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Inquiry

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuy V. Tran whose telephone number is (571) 272-1828. The examiner can normally be reached on M-F (8:00 AM -5:00 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Don Wong can be reached on (571) 272-1834. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

10/31/2005

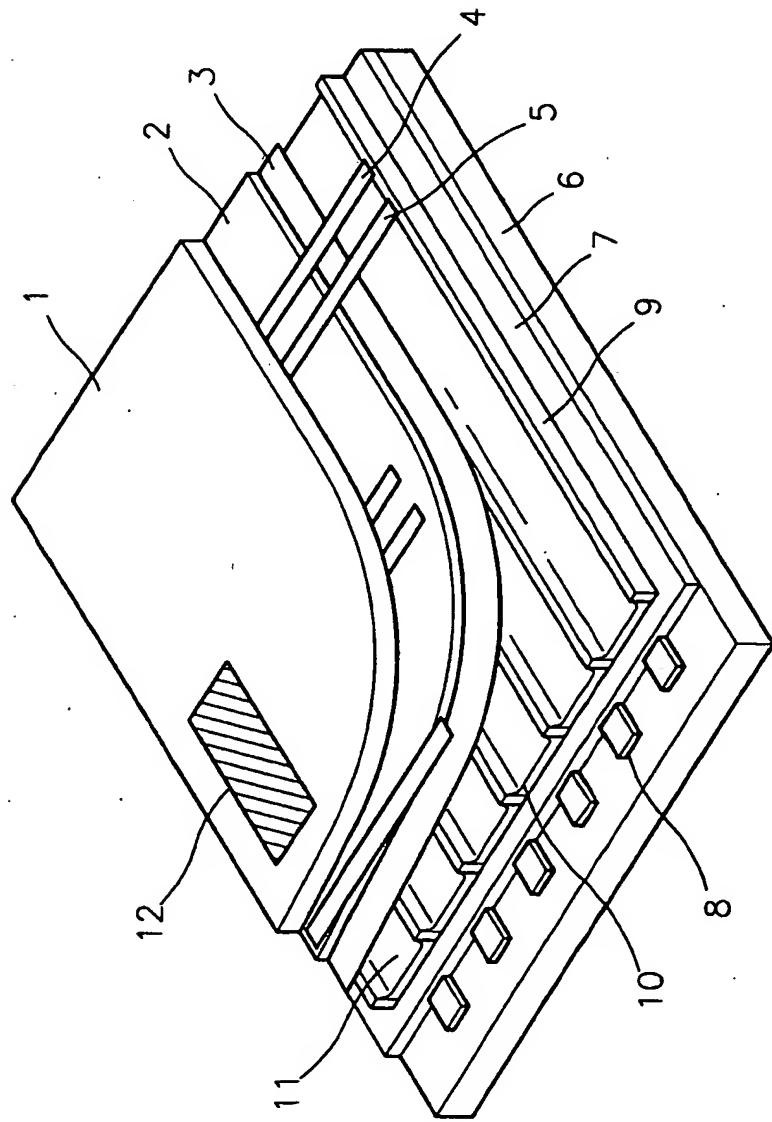


THUY V. TRAN
PRIMARY EXAMINER



Replacement Drawing Sheets
Filing Date: December 30, 2003
Inventors: Jin-Boo SON, et al
**Title: PLASMA DISPLAY PANEL DRIVING METHOD AND APPARATUS
CAPABLE OF REALIZING RESET STABILIZATION**
Application. Serial No. 10/747,674

FIG. 1 (Background Art)



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Title: PLASMA DISPLAY PANEL DRIVING METHOD AND APPARATUS
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FIG. 2 (Background Art)

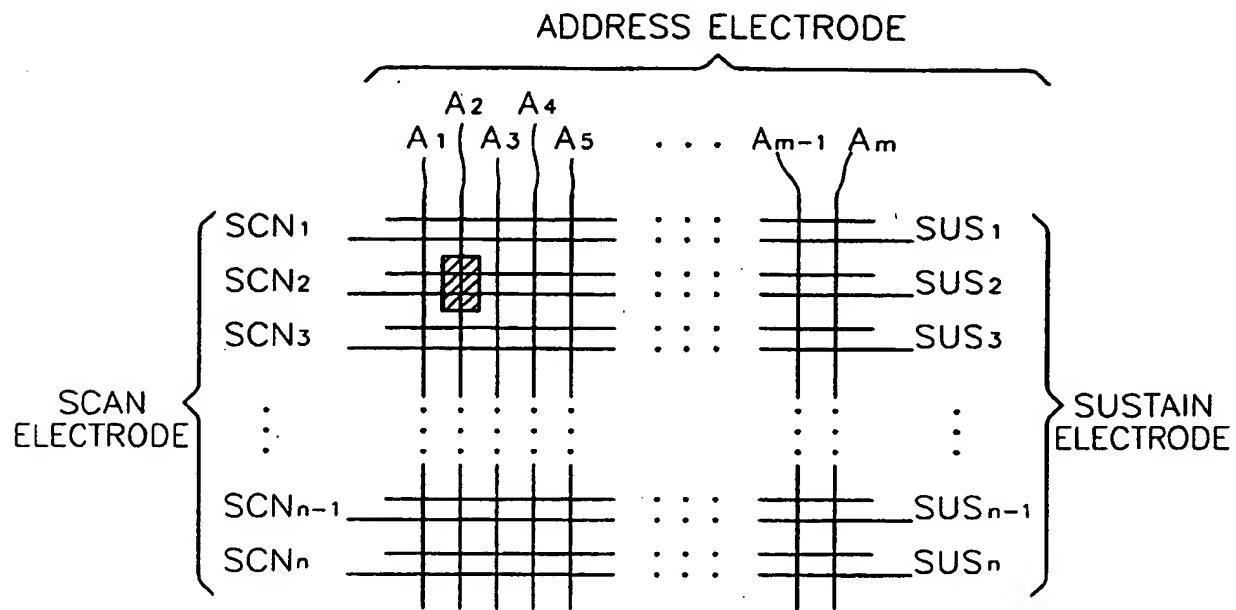


FIG. 3 (Background Art)

